



PATENT APPLICATION

Sheet 1 of 2

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200208051-1	APPLICATION NO.	CONFIRMATION NO.
APPLICANT Benjamin T. Percer, et al.			
FILING DATE h rewth		GROUP	

REFERENCE DESIGNATION




U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	6,351,827	Feb. 26, 2002	Co, et al.
	1B	6,496,790	Dec. 17, 2002	Kathavate, et al.
	1C			
	1D			
	1E			
	1F			
	1G			
	1H			
	1I			
	1J			
	1K			

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
1L				
1M				
1N				
1O				
1P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	Xicor Data Sheet: "X9409 Preliminary Information: Quad Digitally Controlled Potentiometers." Rev 1.3, October 23, 2001.
	1R	Philips Semiconductors Data Sheet: "PCF8575C: Remote 16-bit I/O Expander for I2C-bus." August 5, 1999.
	1S	Integrated Circuit Systems, Inc. Data Sheet: "ICS8431-11: 255MHz, Low Jitter, Crystal Oscillator-to-3.3v LVPECL Frequency Synthesizer." Rev. B, August 7, 2002.

EXAMINER

DATE CONSIDERED

12/8/04

PATENT APPLICATION

Sheet 2 of 2

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200208055-1	APPLICATION NO.	CONFIRMATION NO.
APPLICANT Naysen J. Robertson, t al.			
FILING DATE herewith		GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	2A			
	2B			
	2C			
	2D			
	2E			
	2F			
	2G			
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	2I			
	2J			
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FOREIGN PATENT DOCUMENTS

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2L				
2M				
2N				
2O				
2P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	2Q	AMI Semiconductor Datasheet: "FS7140/FS7145: Programmable Phase-Locked Loop Clock Generator." February 27, 2002.
	2R	National Semiconductor Data Sheet: "LM87: Serial Interface System Hardware Monitor with Remote Diode Temperature Sensing." March 2001.
	2S	

EXAMINER

DATE CONSIDERED

12/8/04